

## Brief Profile

<b>Name:</b>	Mohini Preetam Singh
<b>Date of Birth:</b>	30 /01/1987
<b>Educational Qualification:</b>	
<i>Ph. D.</i>	Amity University, Noida (Pursuing)
<i>M. Tech</i>	Subharti University, Meerut (Microelectronics)
<i>B. Tech</i>	UPTU, Lucknow (ECE)
<b>Work Experience:</b>	
• <i>Teaching</i>	7 Years
• <i>Research/ Industry</i>	-
<b>E-mail ID:</b>	mohini.singh@miet.ac.in
<b>Contact No:</b>	(O): 0121-2439019, 057 (M): 09627999590
<b>Area/Subjects of Interest</b>	Microprocessors, Microcontrollers and Embedded Systems, Digital Image Processing
<b>Teaching:</b>	
<i>Subjects Taught at UG level</i>	Network Analysis and Synthesis, Switching Theory, DCN, EMI, RQM, Microprocessors, Microcontrollers, Embedded Systems.
<i>Subjects Taught at PG level</i>	Microprocessors, Microcontrollers, Embedded Systems.
<b>Research Guidance</b>	
<i>No. of Ph.D./M.Tech Guided</i>	-
<b>Research Publications</b>	
• Journals	4
• Conferences	1
• Book Chapters	-
<b>Patent/IPR</b> (Books Published etc.)	Under Processing on “Microcontrollers for Embedded Systems”
<b>No. of National/International Conferences attended/ Paper Presented</b>	3
<b>STC/FDP/Summer/Winter Schools/Workshops/Seminars attended</b>	10
<b>Memberships of the Professional Societies</b>	CSI, Soft Computing Research Society
<b>Awards/Honors</b>	1) Received Certificate of Excellence for Academic Brilliance during course of Masters. 2) Project on Acoustic Radar Pattern Recognition won <b>MAKE IN INDIA</b> award at Ridhi National Technical Festival in 2016, organized at Roorkee Engineering & Management Technology Institute, Shamli.
<b>Any other relevant Information</b>	

## LIST OF PUBLICATIONS

### Journal Papers

1. Mohini Preetam Singh, Prashant Dixit and Vivek Gupta, “Quantum Confinement Effects on Gate Capacitance in GAA-SNWT” VSRD-IJEECE, Vol. 2 (9), 2012, 745-754.
2. Prashant Dixit, Mohini Preetam Singh and Vivek Gupta, “A Study of Si-Nanowire Transistor in Working Temperature Range” VSRD-IJEECE, Vol. 2 (8), 2012, 630-634.
3. Mohini Preetam Singh, “Regions of Space to predict Earthquake Precursors”, *"AKG International Journal of Technology"* Vol. 7, no.2, 2016.
4. Vaibhav Sharma, Mohini Preetam Singh “Electricity Generation Due to Vibration by Boots” *"AKG International Journal of Technology"* Vol 7, no.1, 2016.

### Conference Papers:

1. Mohini Preetam Singh, “Errors and Testing Approaches in Microprocessors”, in AICTE Sponsored National Conference on Recent Technologies in Electronics (NCRTE-11), Vidya College of Engineering (VCE), Meerut, 2-3 April, 2011.