# Brief Profile

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|---|---|
| **Name:** | Priyanka |
| **Date of Birth:** | 4/01/1992 |
| **Educational Qualification:** | |
| *Ph. D.* | - |
| *M. Tech* | NIT Rourkela, Electronic System & Comm. |
| *B. Tech* | UPTU, Lucknow, ECE |
| **Work Experience:** | |
| • *Teaching* | - |
| • *Research / Industry* | - |
| **E-mail ID:** | priyanka.rajput@miet.ac.in, priyankarajputnit@gmail.com |
| **Contact No:** | +91-9583837038 |
| **Area/Subjects of Interest** | Semiconductor Devices, FinFETs, MOSFETs, Microprocessor, Control System, Signal & System, Digital Electronics. |
| **Teaching:** | |
| *Subjects Taught at UG level* | - |
| *Subjects Taught at PG level* | - |
| **Research Guidance** | |
| *No. of Ph.D./M.Tech Guided* | - |
| **Research Publications** | |
| • Journals | 5 |
| • Conferences | 2 |
| • Book Chapters | - |
| **Patent/IPR** (Books Published etc.) | |
| **No. of National/International Conferences attended/ Paper Presented** | 2 |
| **STC/FDP/Summer/Winter Schools/Workshops/Seminars attended** | 1 |
| **Memberships of the Professional Societies** | - |
| **Awards/Honors** | M.Tech Scholar and Scored Ist Rank at PG level. |
| **Any other relevant Information** | GATE, UGCNET |

# LIST OF PUBLICATIONS

## Journal Papers

I. K P Pradhan, Samar K. Saha,P K Sahu, **Priyanka**, "Impact of Fin Height and Fin Angle variation on the AC performance Matrix of Hybrid FinFETs", *IEEE Transactions of Electronic Devices 64*, 2017, no.1, pp.52-57

II. K P Pradhan, **Priyanka**, P K Sahu, "Temperature dependency of Double Material Gate Oxide (DMGO) Symmetric Dual-k Spacer (SDS) Wavy FinFET",*Superlattices and Microstructures, Elsevier*, 2016,vol. no.89, pp.355-361.

III. K P Pradhan, **Priyanka**, P K Sahu, "Investigation of Asymmetric High-k Underlap Spacer (AHUS) hybrid FinFET from Temperature Perspective",*Microsystem Technologies,Springer*, 2016 ,pp.1-6.

IV. K P Pradhan, **Priyanka**, P K Sahu, "Studyof Fin Tapering in Nanoscale Symmetric Dual-k Spacer (SDS) Hybrid FinFET",*Material Science in Semiconductor Processing,Elsevier*,2016. 57,185-189.

V. K P Pradhan, **Priyanka**, Mallikarjunarao, P K Sahu, "Exploration of Symmetric High-k Spacer (SHS) Hybrid FinFET for High Performance Application." *Superlattices and Microstructures,Elsevier*,2016,vol.no. 90, pp.191-197.

## Conference Papers:

I. K P Pradhan, **Priyanka**, P K Sahu, "Exploration of Double Material Gate Oxide in Symmetric Dual- *k* Spacer Wavy FinFET", *International Conference on Emerging Technologies: Micro to Nano (ETMN)*, Jaipur, 24-25 October, 2015.

II. K P Pradhan, **Priyanka**, Mallikarjunarao, P K Sahu, S K Mohapatra "Analysis of Symmetric High-k Spacer (SHS) Trigate Wavy FinFET: A Novel Device", *12th IEEE India International Conference, IEEE Indicon*, Delhi, 17-20 December, 2015.