

## Brief Profile

<b>Name:</b>	Vishal Verma
<b>Date of Birth:</b>	12-04-1986
<b>Educational Qualification:</b>	
<i>Ph. D.</i>	-
<i>M. Tech</i>	NSIT, New Delhi, (Signal Processing)
<i>B. Tech</i>	UPTU, Lucknow (ECE)
<b>Work Experience:</b>	
• <i>Teaching</i>	07 Years
• <i>Research/Industry</i>	-
<b>E-mail ID:</b>	vishal.verma@miet.ac.in
<b>Contact No:</b>	(M): 8859292966/ 9897112966
<b>Area/Subjects of Interest</b>	Microcontroller, Digital Image Processing, Electronics, Networks Analysis & Synthesis.
<b>Teaching:</b>	
<i>Subjects Taught at UG level</i>	Basic Electronics, Microcontroller, Microprocessor, Networks Analysis & Synthesis, Signals & Systems, Integrated Circuits , Electronics Circuits etc.
<i>Subjects Taught at PG level</i>	-
<b>Research Guidance</b>	
<i>No. of Ph.D./M.Tech Guided</i>	-
<b>Research Publications</b>	
• Journals	01
• Conferences	01
• Book Chapters	-
<b>Patent/IPR</b>	-
(Books Published etc.)	
<b>No. of National/International Conferences attended/Paper Presented</b>	01
<b>STC/FDP/Summer/Winter Schools/Workshops/Seminars attended</b>	13
<b>Memberships of the Professional Societies</b>	ISHRAE- Indian Society of Heating Refrigerating and Air conditioning Engineers IAENG- International Association of Engineers
<b>Awards/Honors</b>	UGC- NET Qualified
<b>Any other relevant Information</b>	B.Tech (Honors)

## LIST OF PUBLICATIONS

### **Journal Papers**

1. Vishal Verma and Mrs Jyotsna Singh, *Digital Image Watermarking Techniques: A Comparative Study*, IJAEEE, Vol. 2, No. 1, March 2013.

### **Conference Papers:**

1. Vishal Verma, "Digital Watermarking," in *1<sup>st</sup> international Conf. on Advancement in Electronics & Communication Engineering(icaece)*., Ghaziabad, March-2013.