

Brief Profile

Name:	H. P .Singh
Date of Birth:	30/06/1980
Educational Qualification:	
<i>Ph. D:</i>	PTU Campus, Jalandhar (Pursuing)
<i>M. Tech :</i>	NITTTR, Chandigarh
<i>B. Tech :</i>	U.P.T.U, Lucknow
Work Experience:	
• <i>Teaching :</i>	10 Years
• <i>Research/ Industry</i>	4 Years
E-mail ID:	hitlendra.singh(@miet.ac.in
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Area/Subjects of Interest :	DSP, Microwave, Digital and Analog Circuits Design, Signals & Systems
Teaching:	
<i>Subjects Taught at UG level:</i>	SSDC, STLD, Network Analysis & Synthesis, Signals & Systems, Digital Logic Design, Electronic Circuits, Electronics Engineering, DSP, Digital Image Processing, Microwave Engineering etc.
<i>Subjects Taught at PG level:</i>	Advanced Digital Signal Processing,
Research Guidance	
<i>No. of Ph.D./M.Tech Guided</i>	-
Research Publications	
• Journals :	04
• Conferences	-
• Book Chapters	-
Patent/IPR (Books Published etc.)	
No. of National/International Conferences attended/ Paper Presented	01
STC/FDP/Summer/Winter Schools/Workshops/Seminars attended :	08
Memberships of the Professional Societies	-
Awards/Honors	-
Any other relevant Information	-

LIST OF PUBLICATIONS

Journal Papers:

1. Anuj Kumar Chauhan and H.P. Singh, "A Review on Self-Interference Cancellation in Full Duplex Communication," Trends in Microwave Engineering and Technology (ManTech Publications), Volume 1, no. 3, December-2016.
2. Hitlendra Pratap Singh and Dr. G.S. Viridi, "A Review on Crosstalk in On-Chip High Speed VLSI Interconnects," International Journal of Scientific & Engineering Research, Volume 7, no. 9, September-2016, pp. 1342-1349, ISSN 2229-5518.
3. Hitlendra Pratap Singh and Dr. Rajesh Mehra, "Crosstalk and Crosstalk Delay Minimization in On-Chip DSM VLSI Interconnects," International Journal of Computer Science & Communication Networks, Vol 2, no. 5, pp. 596-600, October – 2012, ISSN:2249-5789.
4. Hitlendra Pratap Singh and Dr. Rajesh Mehra, "Error Correction and Crosstalk Avoidance Techniques in On-Chip VLSI Interconnects," International Journal of Engineering Research & Technology (IJERT), Vol. 1, no. 8, October – 2012, ISSN: 2278-0181.